

Fig. 1
PRIOR ART

Docket No. 2003P52599US/I331.101.101
MEMORY DEVICE HAVING MULTIPLE ARRAY STRUCTURE FOR INCREASED BANDWIDTH

Jong-Hoon Oh

2/9

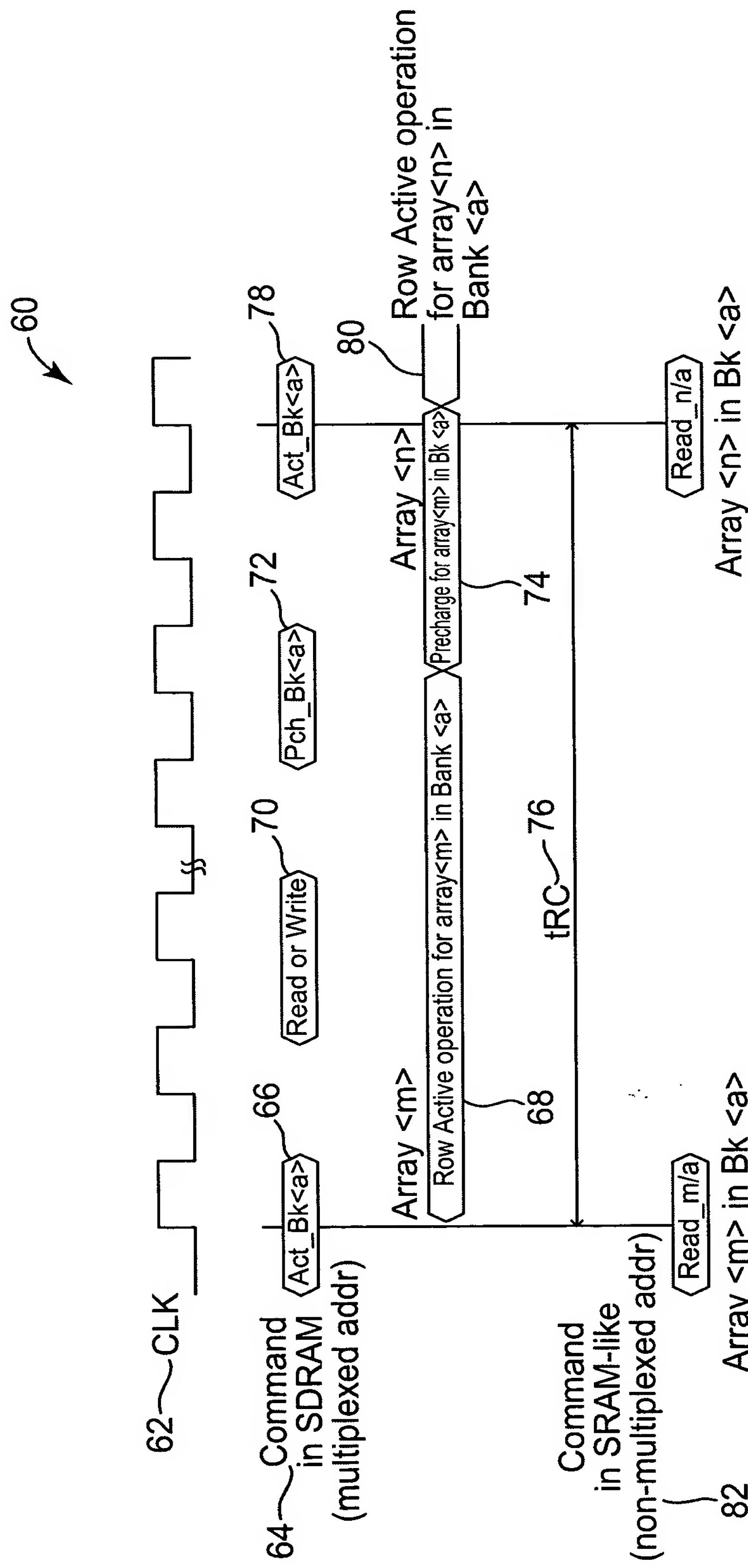


Fig. 2
PRIOR ART

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STRUCTURE FOR INCREASED BANDWIDTH**

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3/9

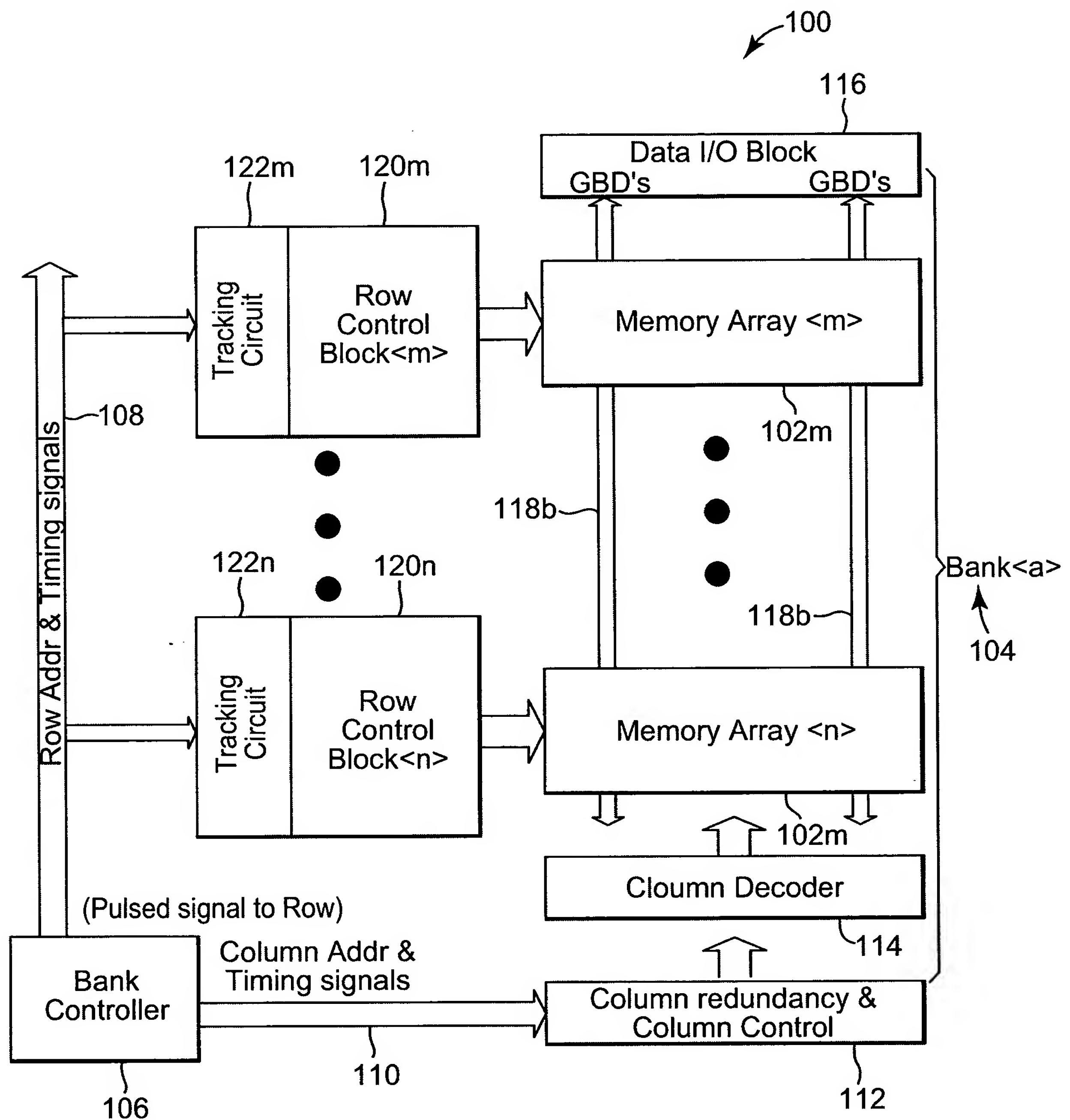


Fig. 3

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4/9

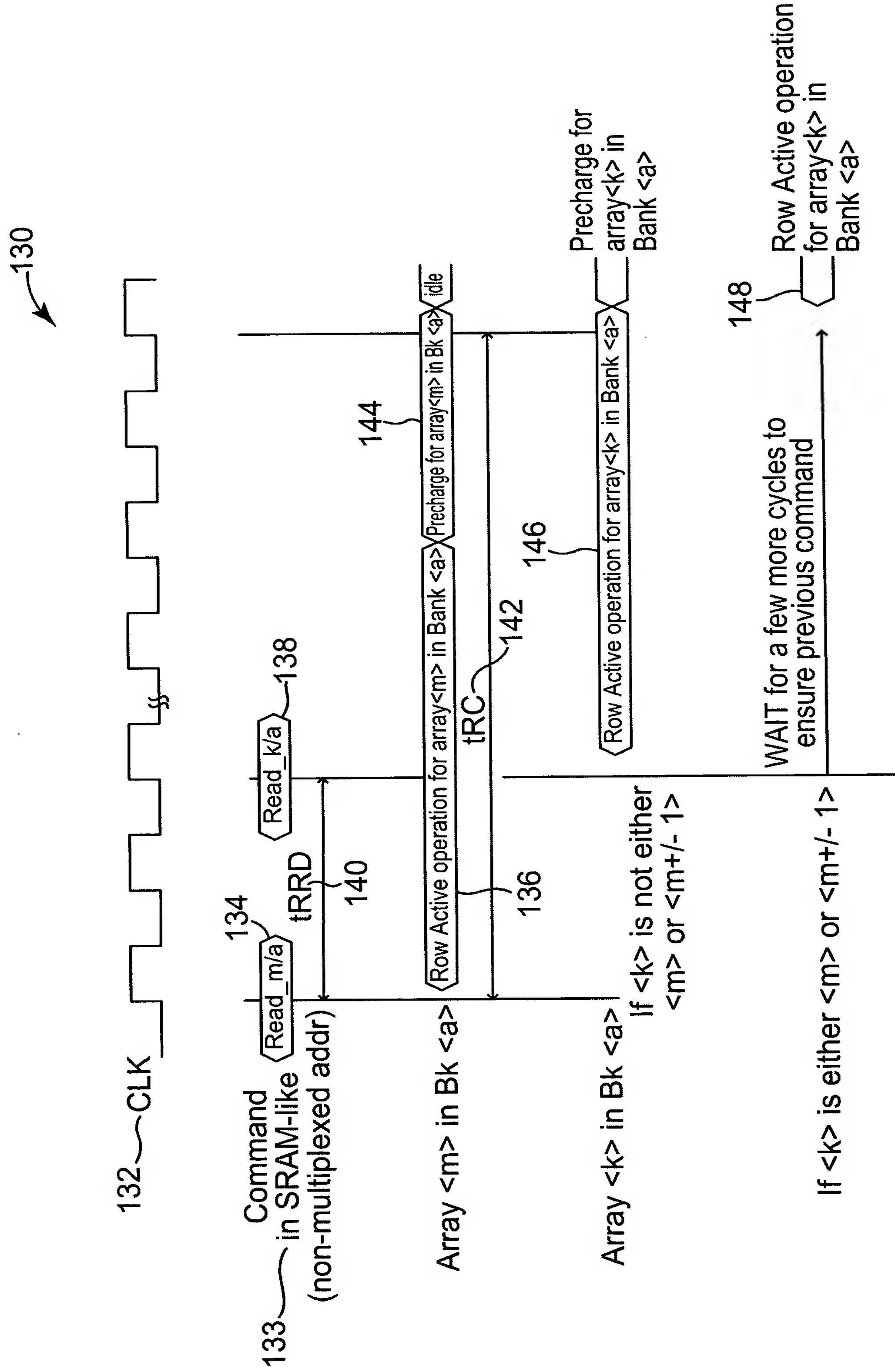


Fig. 4

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5/9

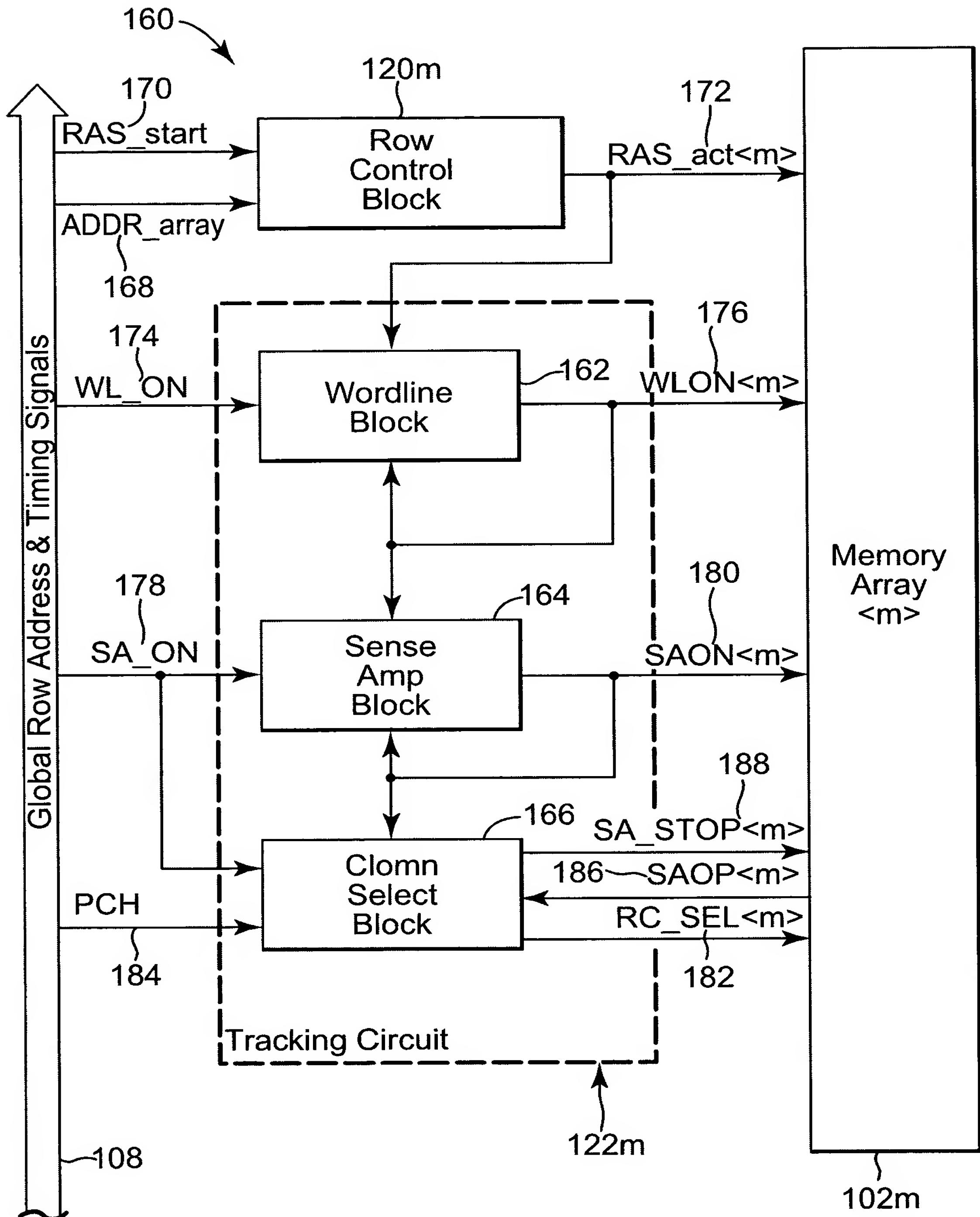


Fig. 5

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 6/9

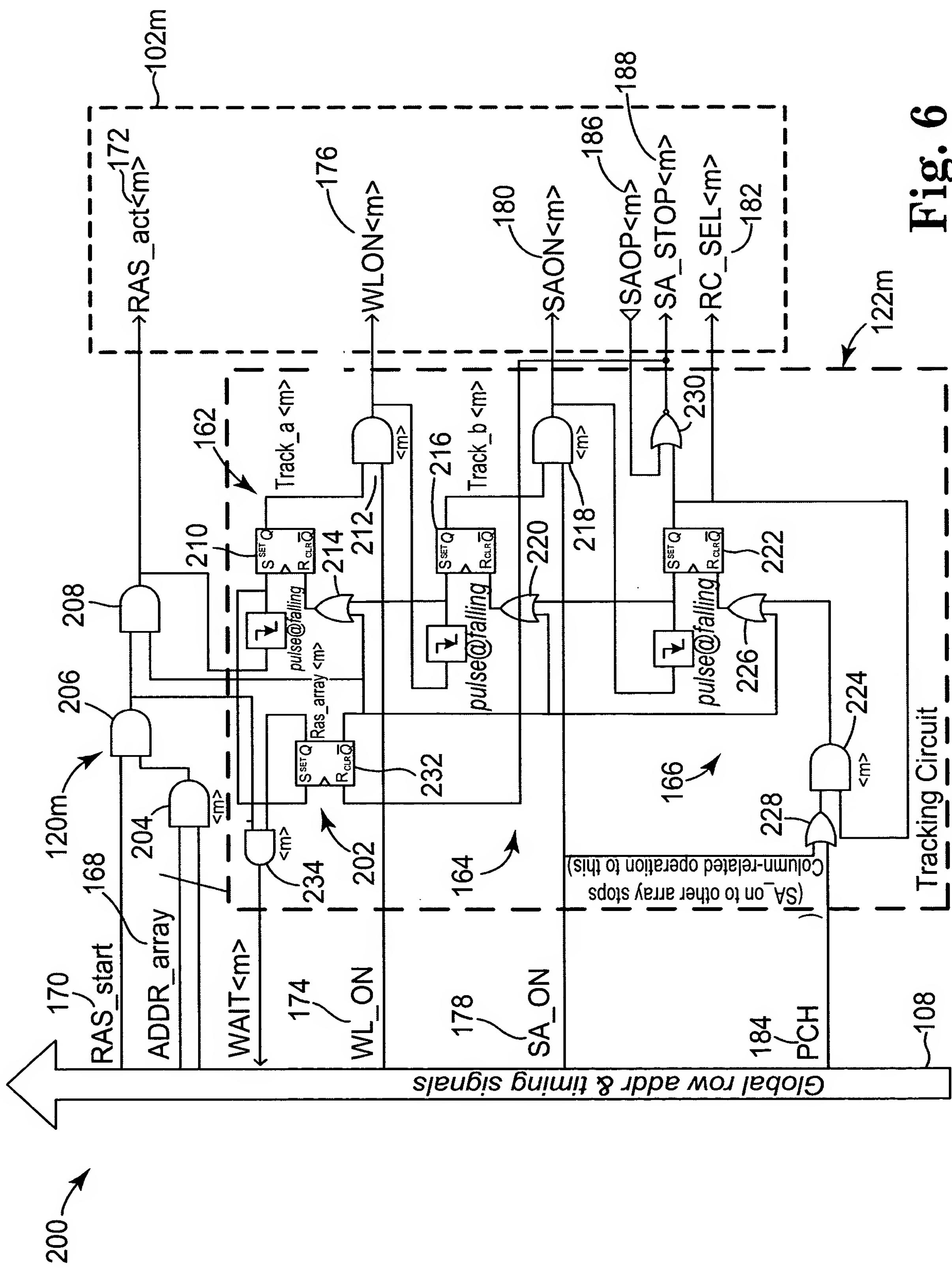


Fig. 6

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 7/9

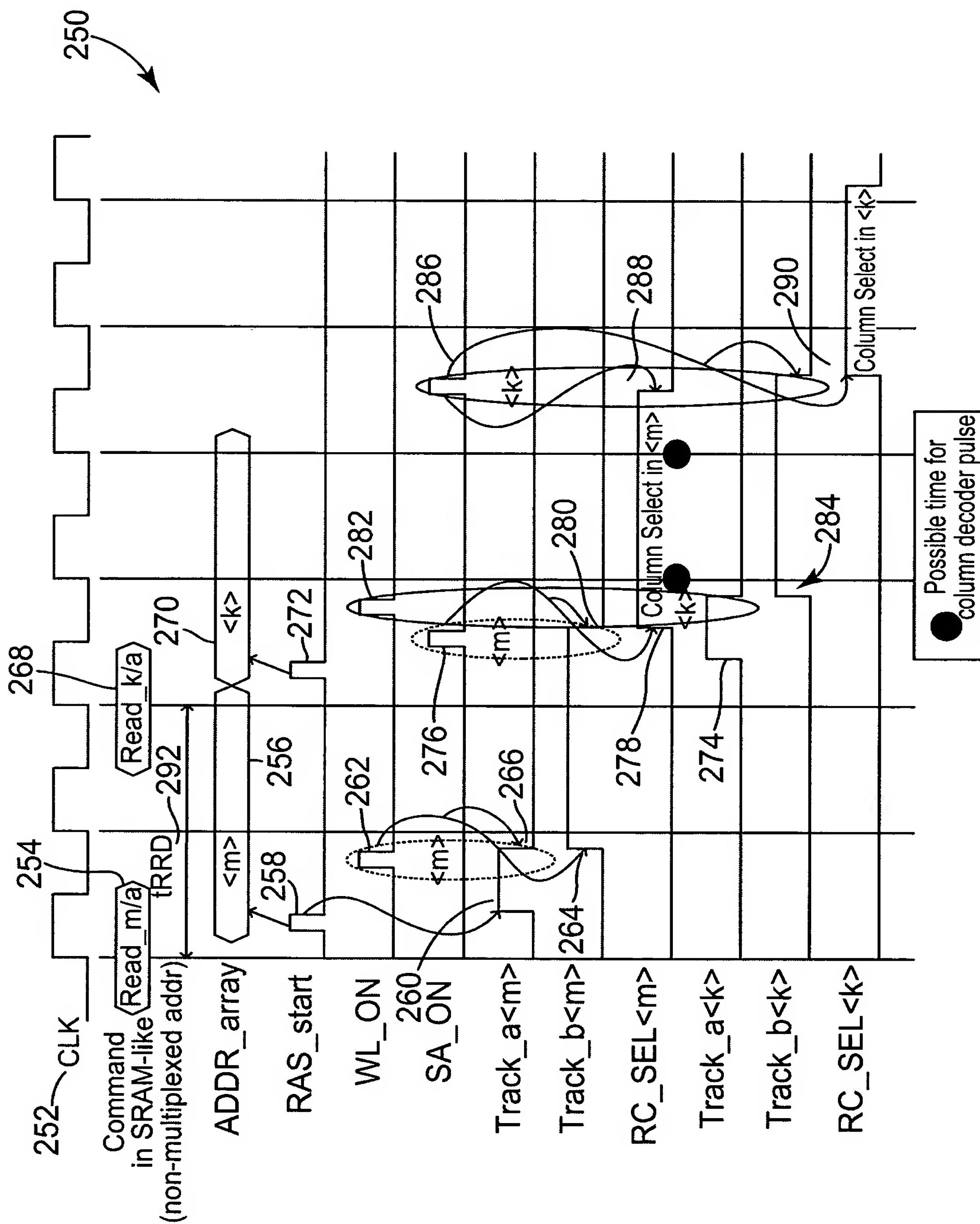


Fig. 7A

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8/9

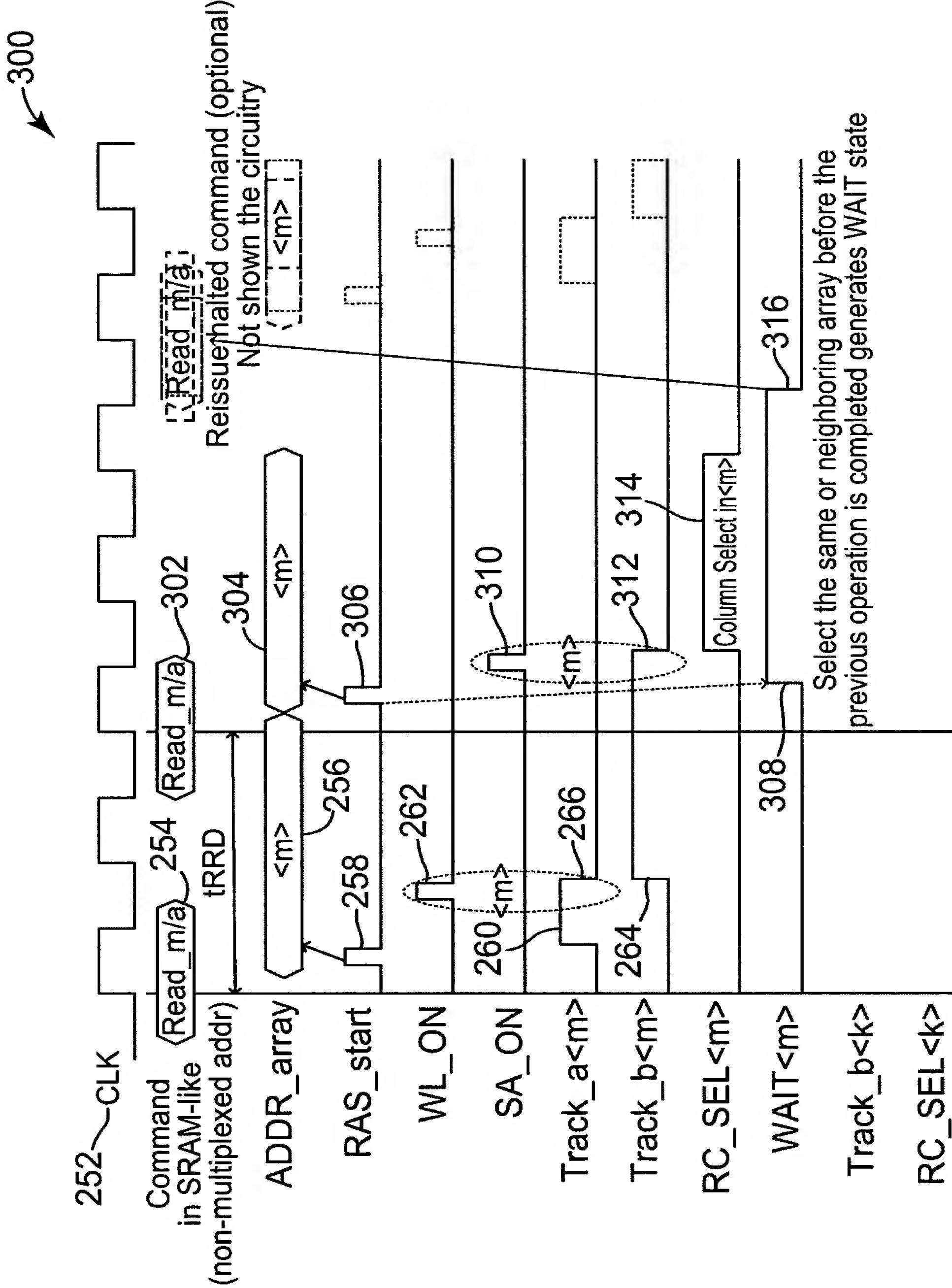
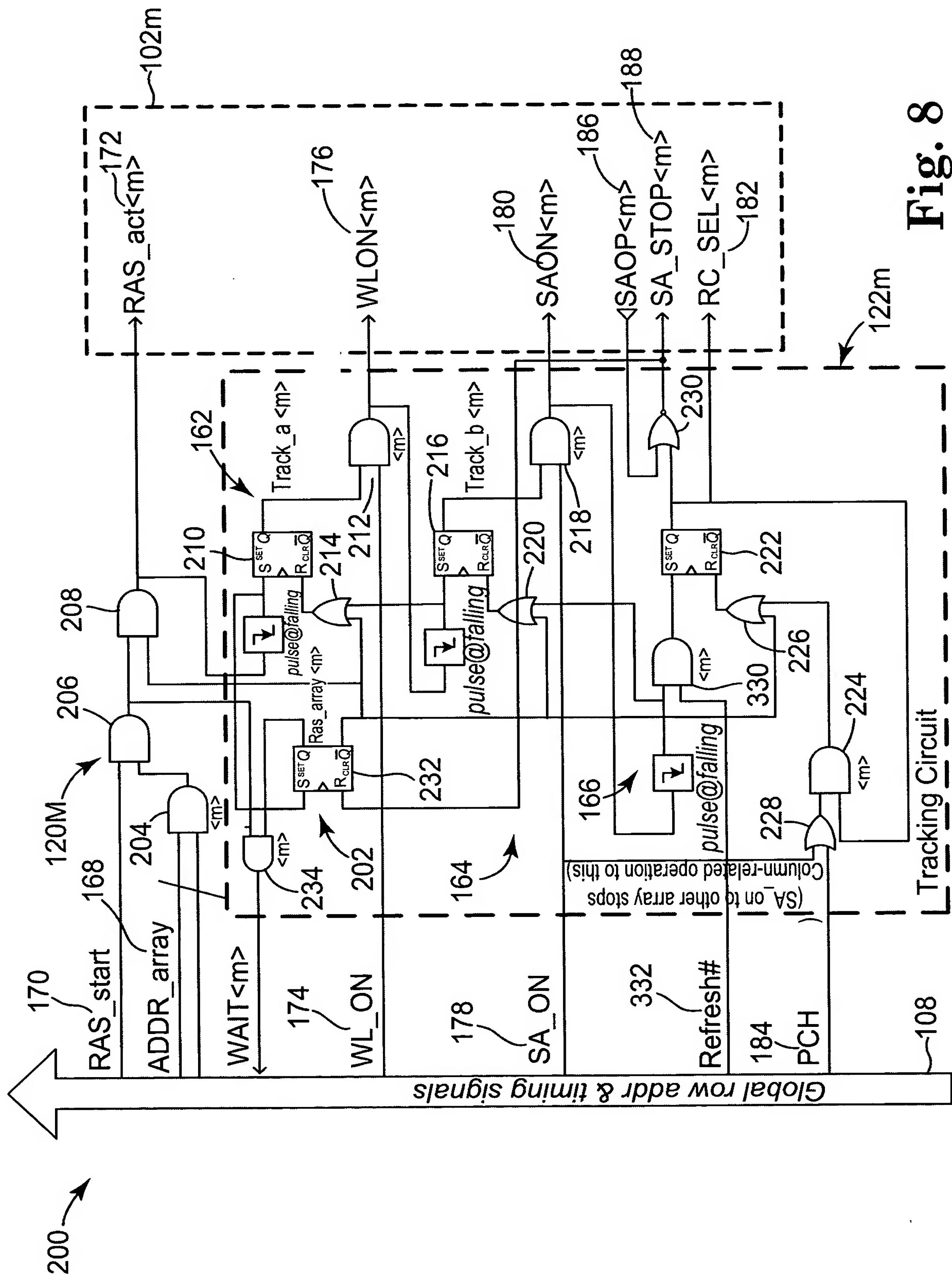


Fig. 7B

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9/9



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